



ASI-T-570EB4AN/D V2.0

Item	Contents	Unit
Size	5.7	inch
Resolution	320(RGB) x 240	/
Technology type	TFT	/
Pixel pitch	0.36 x 0.36	mm
Pixel Configuration	R.G.B. Stripe	
Outline Dimension (W x H x D)	144.0 x 104.6 x 13.0	mm
Active Area	115.2 x 86.4	mm
Display Mode	Super wide view	/
Backlight Type	LED	/



Record of Revision

Date	Revision No.	Summary
2010-04-05	1.0	Rev 1.0 was issued
2014-04-16	1.1	Remove the CN2 in the drawing.
2014-12-18	1.2	Add the LED lifetime.
2017-09-07	1.3	Change LED Backlight "Life Time"
2020-02-13	2.0	Modify pixel pitch page 3 Add CN1 connector page 5 Modify Environmental / Reliability Tests page 15



1. Scope

This data sheet is to introduce the specification of ASI-T-570EB4AN/D V2.0 active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC and a backlight unit. The 5.7" display area contains 320(RGB) x 240 pixels.

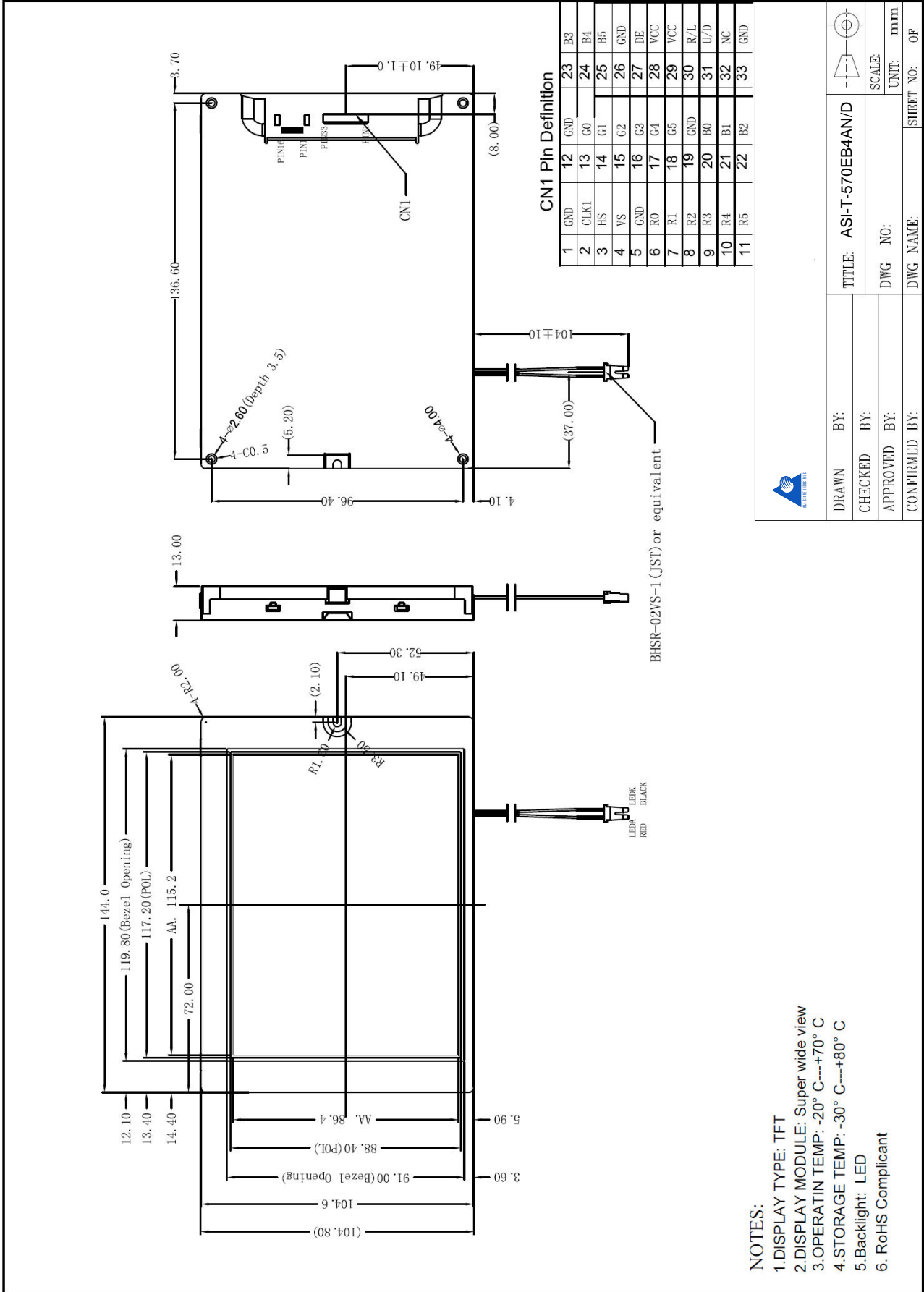
2. Application

Digital equipments which need color display, medical equipments and industrial equipments.

3. General Information

Item	Contents	Unit
Size	5.7	inch
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4. Outline Drawing



5. Interface signals

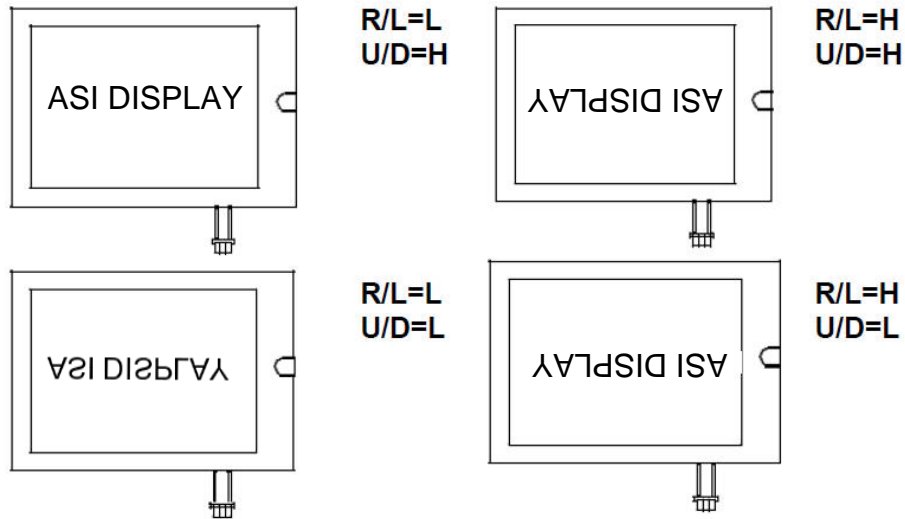
Pin No	Symbol	Function	Remark
1	GND	Ground for logic circuit	
2	CLK1	Data sampling clock	
3	HS (HSYNC)	Horizontal synchronous signal	
4	VS (VSYNC)	Vertical synchronous signal	
5	GND	Ground for logic circuit	
6	R0	Red pixel data(LSB)	
7	R1	Red pixel data	
8	R2	Red pixel data	
9	R3	Red pixel data	
10	R4	Red pixel data	
11	R5	Red pixel data(MSB)	
12	GND	Ground for logic circuit	
13	G0	Green pixel data(LSB)	
14	G1	Green pixel data	
15	G2	Green pixel data	
16	G3	Green pixel data	
17	G4	Green pixel data	
18	G5	Green pixel data(MSB)	
19	GND	Ground for logic circuit	
20	B0	Blue pixel data(LSB)	
21	B1	Blue pixel data	
22	B2	Blue pixel data	
23	B3	Blue pixel data	
24	B4	Blue pixel data	
25	B5	Blue pixel data(MSB)	
26	GND	Ground for logic circuit	
27	DE	Data Enable (connected to GND, if sync mode)	
28	VCC	Power Supply : +3.3V	
29	VCC	Power Supply : +3.3V	
30	R/L	Horizontal display mode select signal Left / Right Scan control input	Note 2
31	U/D	Vertical display mode select signal Up / Down Scan control input	Note 2
32	NC	No Connection	
33	GND	Ground for logic circuit	

CN1 Connector: 08-6210-033-340-800 (ELCO) or equivalent.

Notes :

1 .The horizontal display start timing is settled in accordance with a rising timing of DE signal. Don't keep DE "High" during operation.

2.



6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VCC	-0.3	+7.0	V	
Logic supply Voltage	Vi	-0.3	Vcc+0.3	V	

6.2. Environment Conditions

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply voltage	VCC	+3.0	+3.3	+3.6	V	
Power Supply Current	ICC		130	150	mA	VCC =3.3V
Input Signal Voltage	VIL	0V	--	0.3VCC	V	
	VIH	0.7VCC	--	3.6	V	

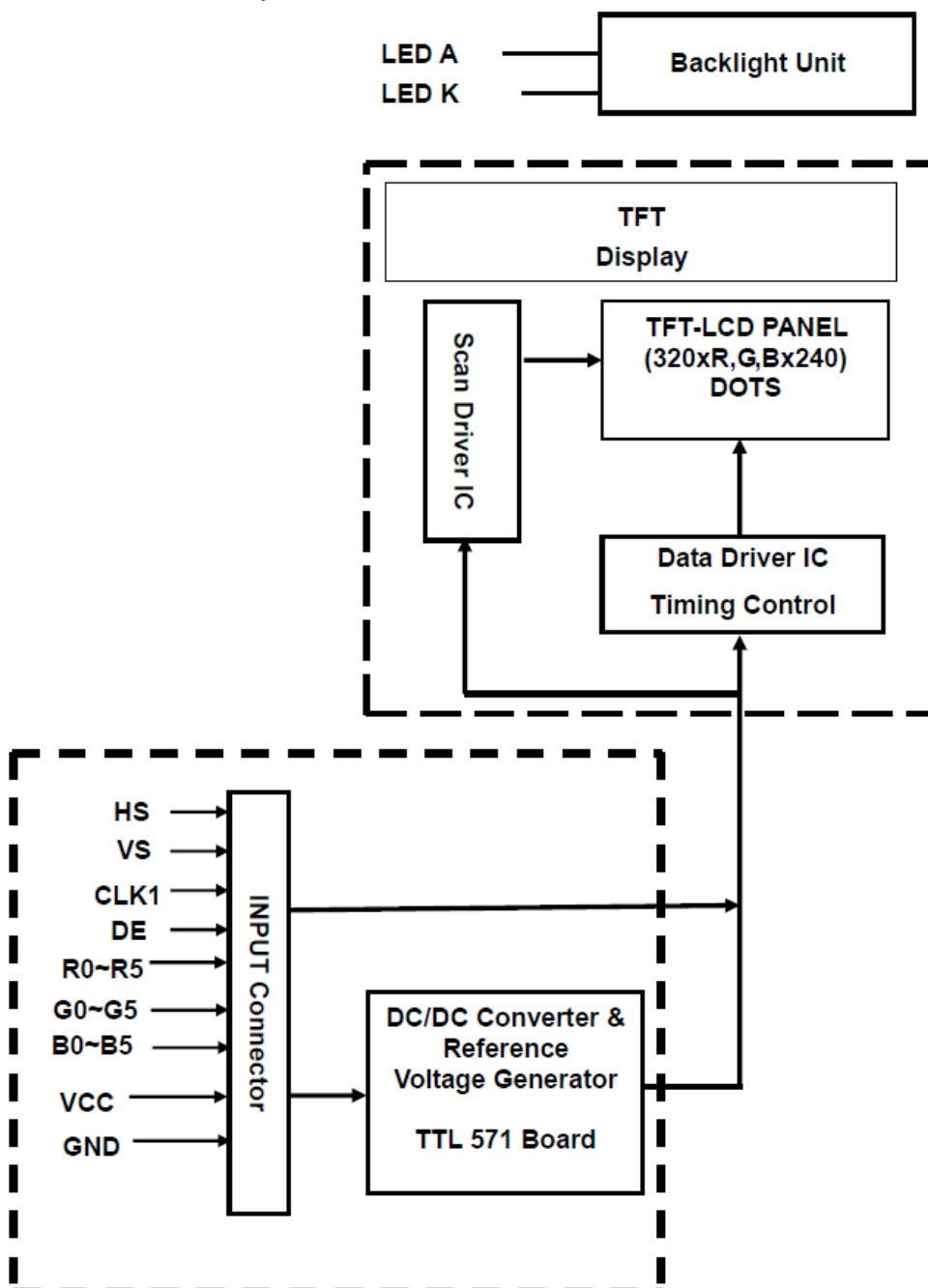
7.2 LED Backlight

Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IF	-	140	-	mA	VCC =3.3V
Forward Voltage	VLED	--	10.2	10.8	V	
LED Lifetime	--	--	50,000	--	hrs	Note

Note: The LED lifetime is defined as the LED dice brightness decrease to 50% original brightness that the ambient temp. is 22degreeC and LED dice current 20mA.

7.3 Schematic of LCD module system



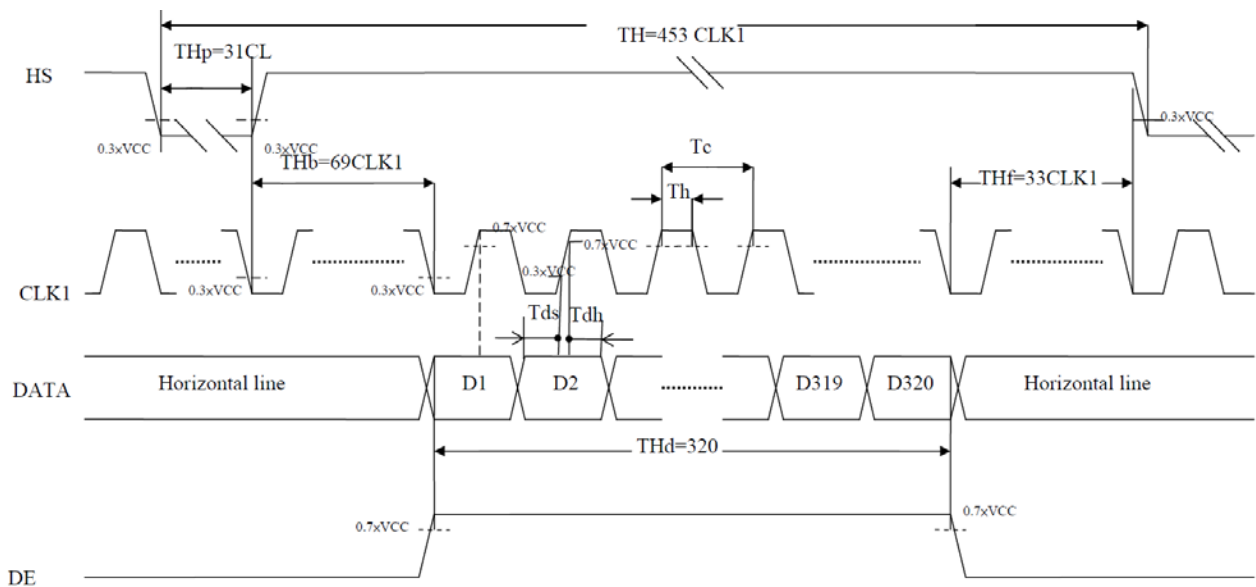
8. Command/AC Timing

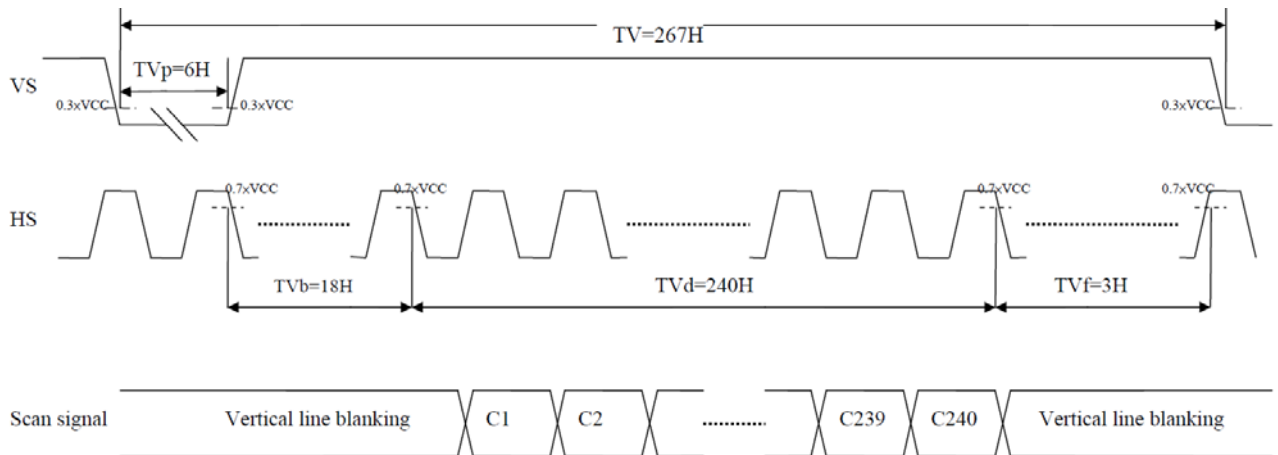
8.1 Input Signal Timing Specifications

Parameter		Symbol	MIN.	TYP.	MAX.	Unit	Remarks
CLK	Frequency	1/Tc		7.21		MHz	
	Duty ratio	Th/Tc	40	50	60	%	
DATA	Setup time	Tds	12			ns	
	Hold time	Tdh	12			ns	
Horizontal synchronizing	Period	TH	--	453	--	Clock	
	Pulse width	THp	--	31	--	Clock	
	Horizontal period	THd	--	320	--	Clock	
	Blank porch	THb	--	69	--	Clock	
	Front porch	THf	--	33	--	Clock	
Vertical synchronizing	Period	TV	--	267	--	Line	
	Pulse width	TVp	--	6	--	Line	
	Vertical period	TVd	--	240	--	Line	
	Blank porch	TVb	--	18	--	Line	
	Front porch	TVf	--	3	--	Line	

Notes:

1. In case of using the slow frequency, the deterioration of display flicker etc may occur.
2. The timing characteristics are basically fixed as above.



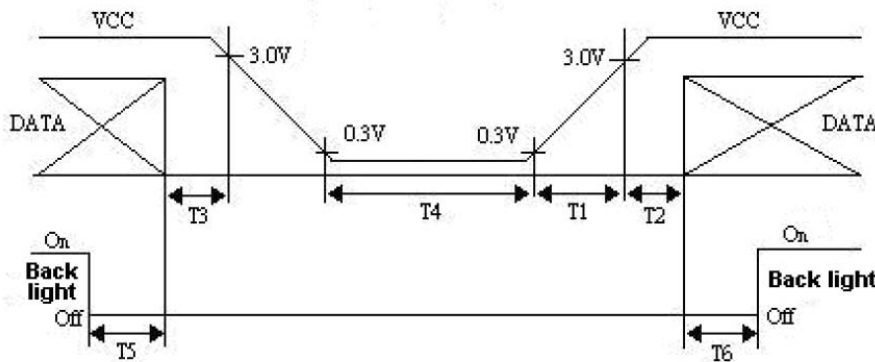


8.2 Color Data Input Assignment

		Data Signal																Correspondence between Data and Display Position		
		Red						Green						Blue						
Color		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray Scale of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
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	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	
Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0		
Gray Scale of Green	Green(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	
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	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0	
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0	
Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0		
Gray Scale of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Blue (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	
	Blue (2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	
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	Blue (61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1	
	Blue (62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0	
Blue (63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		

S960	S959	S958	S957	S956	S955	S954	S953	-----				S002	S001	
C001	B320	G320	R320	B319	G319	R319	B318	G318	-----				G001	R001
⋮									-----					
C240	B320	G320	R320	B319	G319	R319	B318	G318	-----				G001	R001

8.3 Power Off/On Sequence Timing



Timing Specifications:
 $0 < T1 \leq 15\text{ms}$
 $T2 > 0.5\text{S}$
 $0 < T3 \leq 0.1\text{S}$
 $T4 > 1\text{S}$
 $T5 > 0.1\text{S}$
 $T6 > 0.1\text{S}$

9. Optical Specification

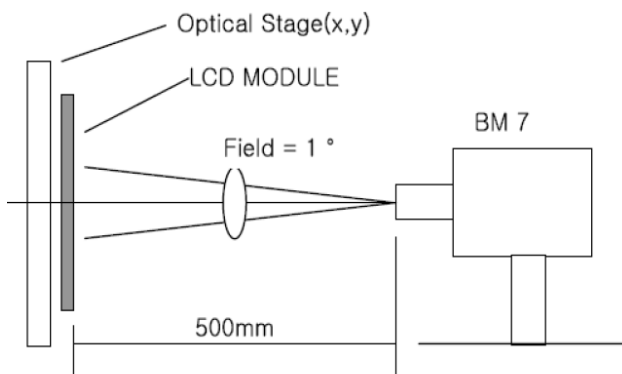
Ta=25°C

Item	Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio	CR	$\theta=0^\circ$	300	350	-		Note1 Note2
Response Time	Ton/ Toff	25°C	-	15	30	ms	Note1 Note3
				35	50		
View Angles	θT	$CR \geq 10$	60	70	-	Degree	Note 4
	θB		40	50	-		
	θL		60	70	-		
	θR		60	70	-		
Chromaticity	White	Brightness is on	Typ-0.5	0.30	Typ+0.5		Note5, Note1
				0.35			
Luminance	L		360	400	-	cd/m ²	Note1 Note6
Uniformity	U		75	-	-	%	Note1 Note7

Note : Ambient temperature =25°C. LED current = 140 mA.

Note 1: Definition of optical measurement system.

Temperature = 25°C(±3°C); LED back-light: ON, Environment brightness < 150 lx

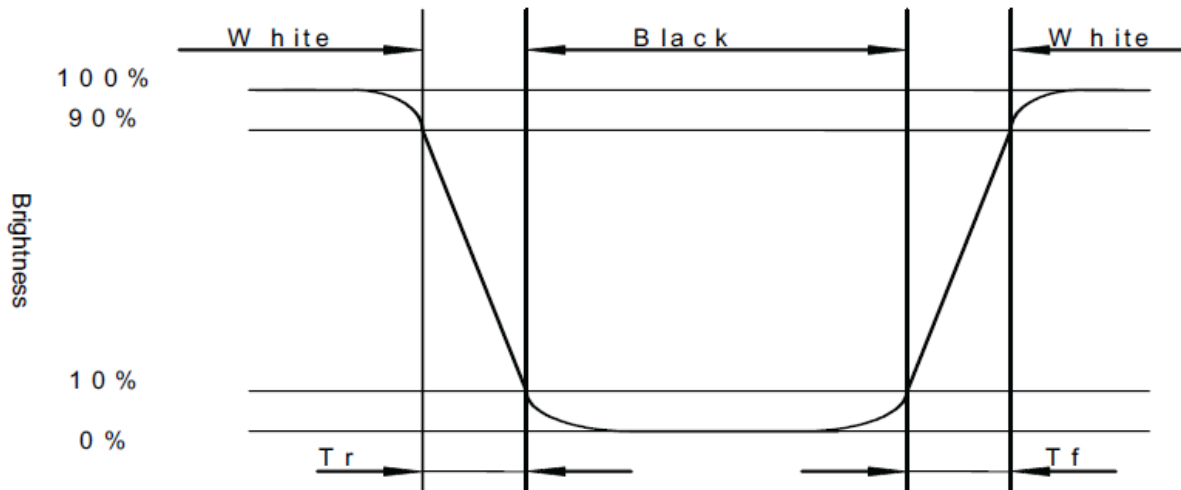


Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

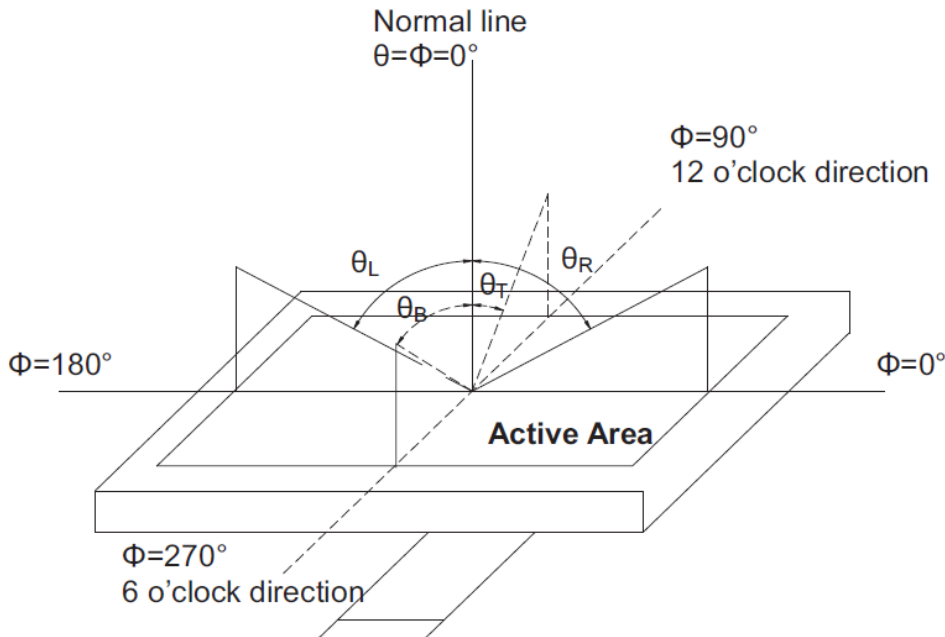
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, T_r) and from white to black (Decay Time, T_f).



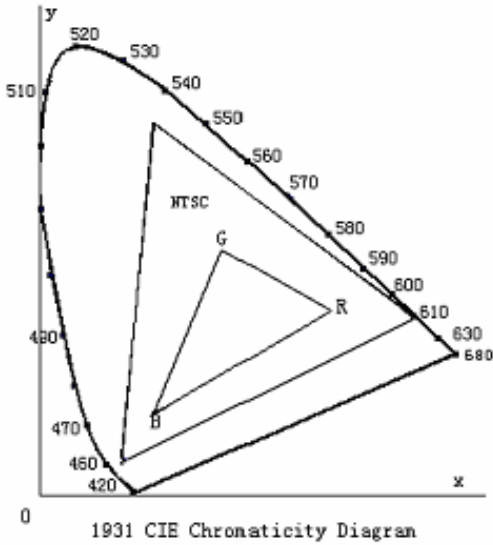
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels “White” at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$$

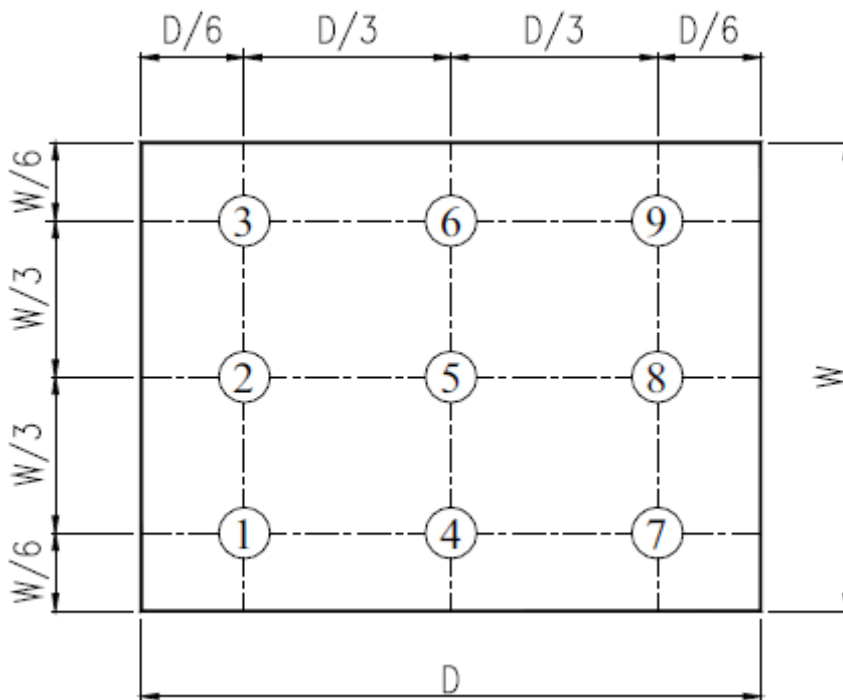


Fig. 2 Definition of uniformity

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ta=+70°C, 240hrs	IEC68-2-2
2	Low Temp Operation	Ta=-20°C, 240hrs	IEC68-2-1
3	High Temp Storage	Ta=+80°C, 240hrs	IEC68-2-2
4	Low Temp Storage	Ta=-30°C, 240hrs	IEC68-2-1
5	High Temp & High Humidity Storage	Ta=+60°C, 90% RH, 240 hours	IEC68-2-3
6	Thermal Shock (Non-operation)	-30°C → +25°C → +80°C, 200 cycles 30 min 5min 30min	IEC68-2-14
7	ESD (Operation)	150pF, R=330Ω Air:±15KV; Contact:±8KV 10 times/point; 4 points/panel face	IEC-61000-4-2
8	Vibration (Non-operation)	Frequency :0~55Hz, Amplitude: 1.5m Sweep Time:11min Test period: 6 cycles for each Direction of X, Y, Z.	IEC68-2-6

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

11. Precautions for Use of LCD Modules

11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling

A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability

C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.

D. Provide a space so that the panel does not come into contact with other components.

E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.

F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.

G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.

H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

A. Ground soldering iron tips, tools and testers when they are in operation.

B. Ground your body when handling the products.

C. Power on the LCD module before applying the voltage to the input terminals.

D. Do not apply voltage which exceeds the absolute maximum rating.

E. Store the products in an anti-electrostatic bag or container.

11.4 Storage

A. Store the products in a dark place at $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ with low humidity (40% RH to 60% RH).

Don't expose to sunlight or fluorescent light.

B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

A. Do not wipe the touch panel with dry cloth, as it may cause scratch.

B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

A. Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

B. In order to make the display assembly stable and firm, ASI recommends to design some supporting at the display backside, especially for the display with tape-attached touch panel, such supporting is important and essential, or else, the display may drop-off from front after some period of time.

C. Do not display the fixed pattern for a long time because it may develop image sticking due to the LCD structure. If the screen is displayed with fixed pattern, use a screen saver.

